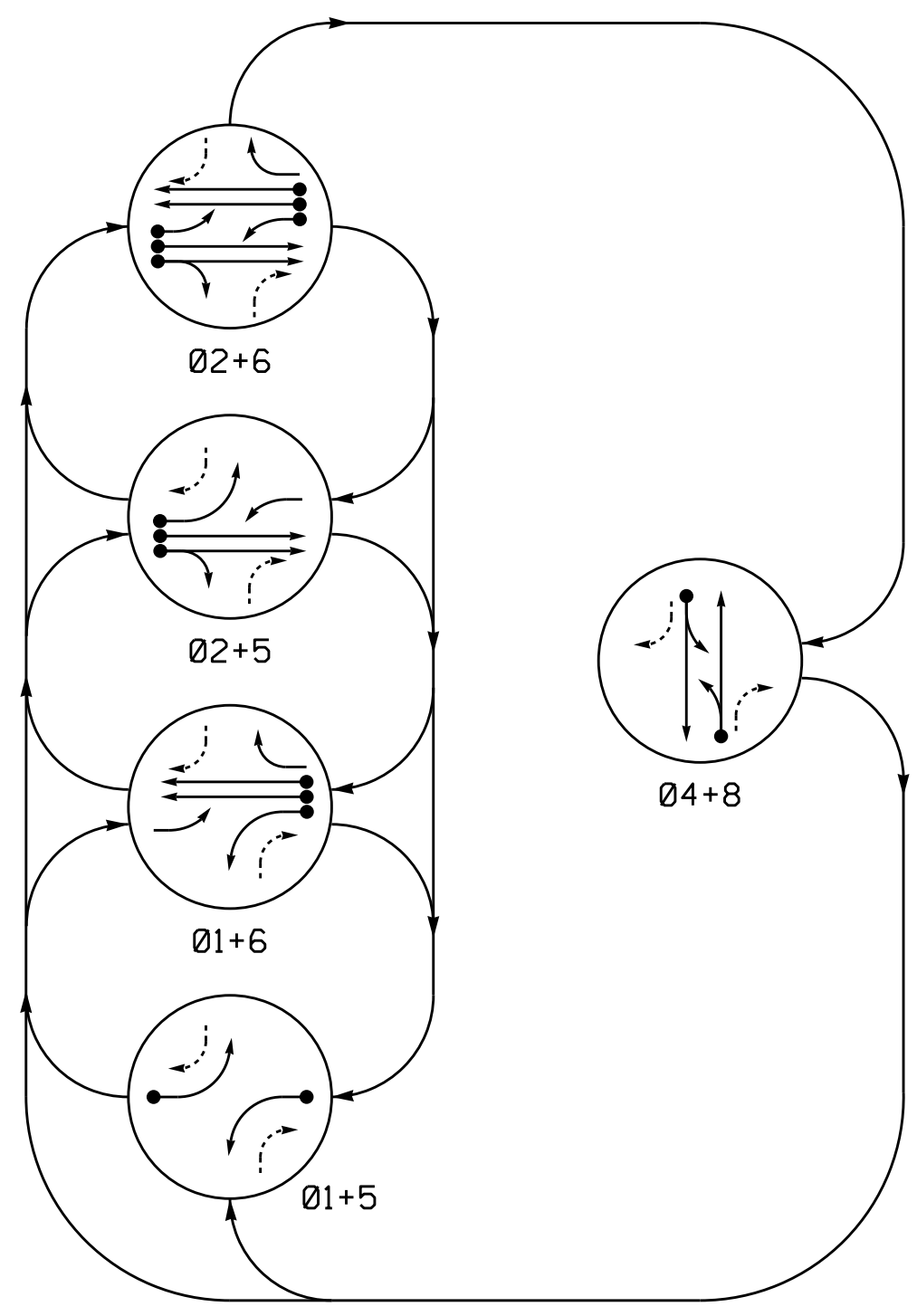


**This electronic collection of documents is provided  
for the convenience of the user  
and is Not a Certified Document –**

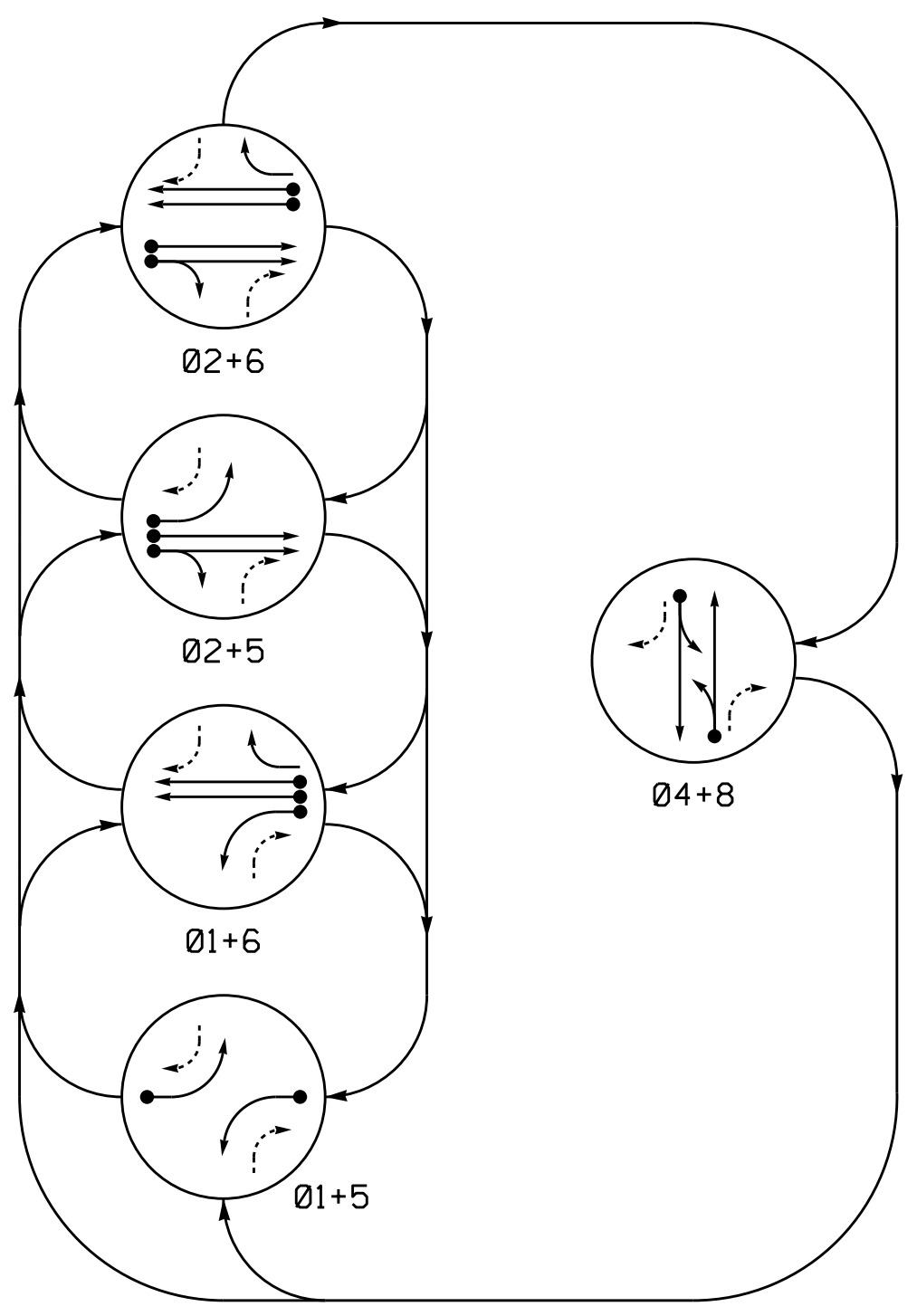
**The documents contained herein were originally issued  
and sealed by the individuals whose names and license  
numbers appear on each page, on the dates appearing  
with their signature on that page.**

**This file or an individual page  
shall not be considered a certified document.**

DEFAULT PHASING DIAGRAM



ALTERNATE PHASING DIAGRAM



**PHASING DIAGRAM DETECTION LEGEND**  
 ● DETECTED MOVEMENT  
 ○ UNDETECTED MOVEMENT (OVERLAP)  
 - - - UNSIGNALIZED MOVEMENT  
 <- - - PEDESTRIAN MOVEMENT

**DEFAULT PHASING TABLE OF OPERATION**

SIGNAL FACE	PHASE					
	01+5	01+6	02+5	02+6	04+8	F L H S
11	←	←	←	←	←	←
22, 23	R	R	G	G	R	Y
41, 42, 43	R	R	R	R	G	R
51	←	←	←	←	←	←
62, 63	R	G	R	G	R	Y
81, 82, 83	R	R	R	R	G	R

**ALTERNATE PHASING TABLE OF OPERATION**

SIGNAL FACE	PHASE					
	01+5	01+6	02+5	02+6	04+8	F L H S
11	←	←	←	←	←	←
22, 23	R	R	G	G	R	Y
41, 42, 43	R	R	R	R	G	R
51	←	←	←	←	←	←
62, 63	R	G	R	G	R	Y
81, 82, 83	R	R	R	R	G	R

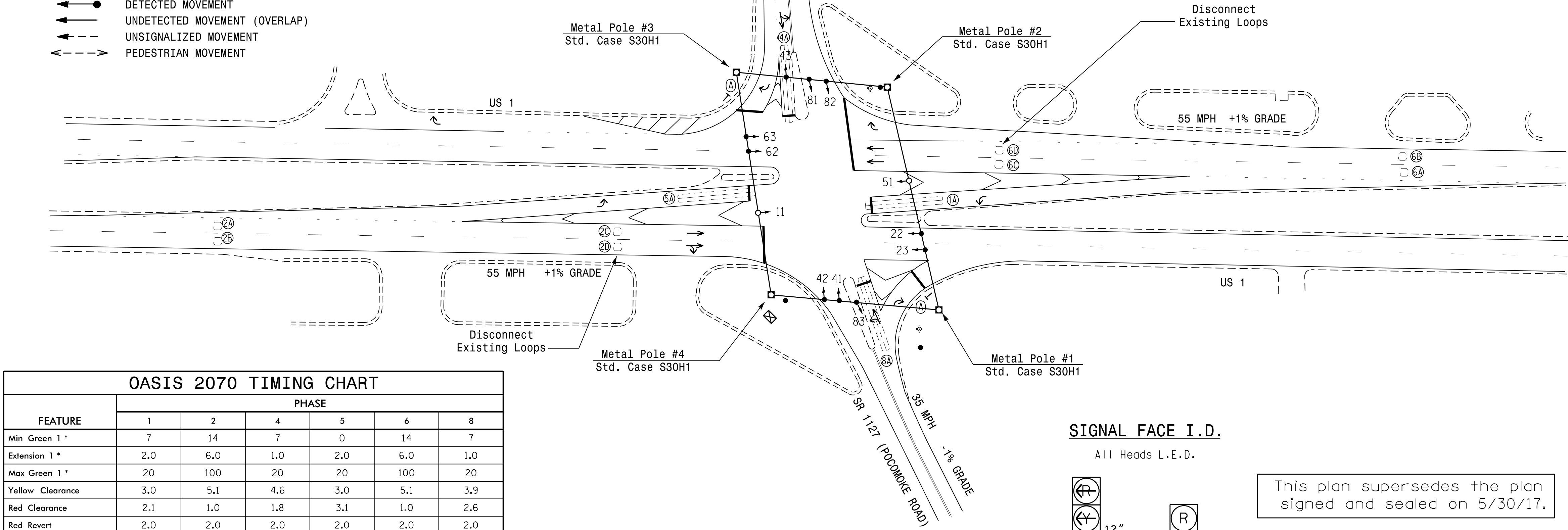
**OASIS 2070 LOOP & DETECTOR INSTALLATION CHART**

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING					SYSTEM LOOP	NEW CARD	
					PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME			DELAY TIME
1A	6X60	2-4-2	+5	-	1	Y	Y	-	-	15 *	-	Y
2A	6X6	5	420	-	2	Y	Y	-	-	3	-	Y
2B	6X6	5	420	-	2	Y	Y	-	-	-	-	Y
2C, 2D	6X6	3	110	-	DISCONNECT					-	-	
4A	6X60	2-4-2	+5	-	4	Y	Y	-	-	-	-	Y
5A	6X60	2-4-2	+5	-	5	Y	Y	-	-	15 *	-	Y
6A	6X6	5	420	-	6	Y	Y	-	-	-	-	Y
6B	6X6	3	110	-	6	Y	Y	-	-	-	-	Y
6C, 6D	6X60	2-4-2	+5	-	DISCONNECT					-	-	
8A	6X60	2-4-2	+5	-	8	Y	Y	-	-	-	-	Y

\* Disable Delay During Alternate Phasing Operation.  
 # Disable Phase Call For Loop(s) During Alternate Phasing Operation.

5 Phase Fully Actuated (Isolated)

- NOTES**
- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
  - Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
  - Phase 1 and/or phase 5 may be lagged.
  - Renumber existing loop 2E as 5A and loop 6E as 1A.
  - Disconnect existing loops 2C, 2D, 6C, and 6D to utilize Volume Density Operation.
  - Set all detector units to presence mode.
  - In the event of loop replacement, refer to the current ITS and Signals Design Manual and submit a Plan of Record to the Signal Design Section.
  - Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
  - Pavement markings are existing.
  - The Division Traffic Engineer will determine the hours of use for each phasing plan.

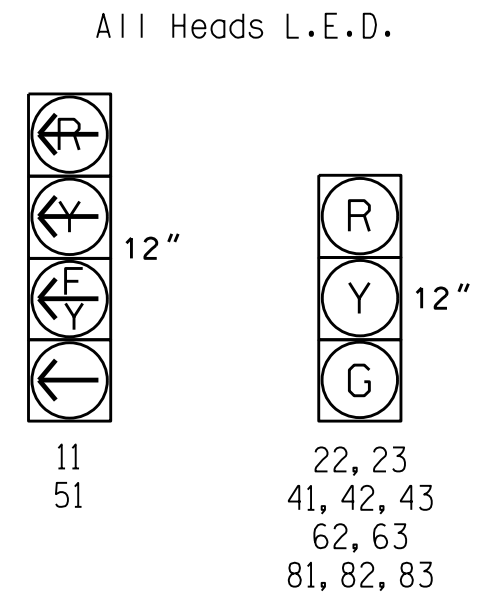


**OASIS 2070 TIMING CHART**

FEATURE	PHASE					
	1	2	4	5	6	8
Min Green 1 *	7	14	7	0	14	7
Extension 1 *	2.0	6.0	1.0	2.0	6.0	1.0
Max Green 1 *	20	100	20	20	100	20
Yellow Clearance	3.0	5.1	4.6	3.0	5.1	3.9
Red Clearance	2.1	1.0	1.8	3.1	1.0	2.6
Red Revert	2.0	2.0	2.0	2.0	2.0	2.0
Walk 1 *	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-
Seconds Per Actuation *	-	1.5	-	-	1.5	-
Max Variable Initial *	-	46	-	-	46	-
Time Before Reduction *	-	30	-	-	30	-
Time To Reduce *	-	454	-	-	45	-
Minimum Gap	-	3.4	-	-	3.4	-
Recall Mode	-	MIN RECALL	-	-	MIN RECALL	-
Vehicle Call Memory	-	YELLOW	-	-	YELLOW	-
Dual Entry	-	-	ON	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

**SIGNAL FACE I.D.**



This plan supersedes the plan signed and sealed on 5/30/17.

**LEGEND**

PROPOSED	EXISTING
○ Traffic Signal Head	● N/A
○ Modified Signal Head	N/A
⊥ Sign	⊥
⊥ Pedestrian Signal Head With Push Button & Sign	⊥
⊥ Signal Pole with Guy	⊥
⊥ Signal Pole with Sidewalk Guy	⊥
⊠ Inductive Loop Detector	⊠
⊠ Controller & Cabinet	⊠
⊠ Junction Box	⊠
- - - 2-in Underground Conduit	- - -
N/A Right of Way	- - -
→ Directional Arrow	→
○ Metal Strain Pole	○
N/A Fire Hydrant	○
⊠ "YIELD" Sign (R1-2)	⊠

**Signal Upgrade**

Prepared In the Offices of:  
 TRANSPORTATION MOBILITY AND SAFETY DIVISION  
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION  
 Signal Design Section  
 750 N. Greenfield Pkwy, Garner, NC 27529

**US 1 at SR 1127 (Pocomoke Road)**

Division 5 Franklin County Franklinton

PLAN DATE: May 2017 REVIEWED BY:  
 PREPARED BY: R.J. Ziembra REVIEWED BY:

REVISIONS INIT. DATE

SCALE 0 50  
 1"=50'

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
 NORTH CAROLINA PROFESSIONAL ENGINEER  
 SEAL 026486  
 ROBERT J. ZIEMBA  
 ENGINEER

2/20/2018  
 DATE

SIG. INVENTORY NO. 05-0208

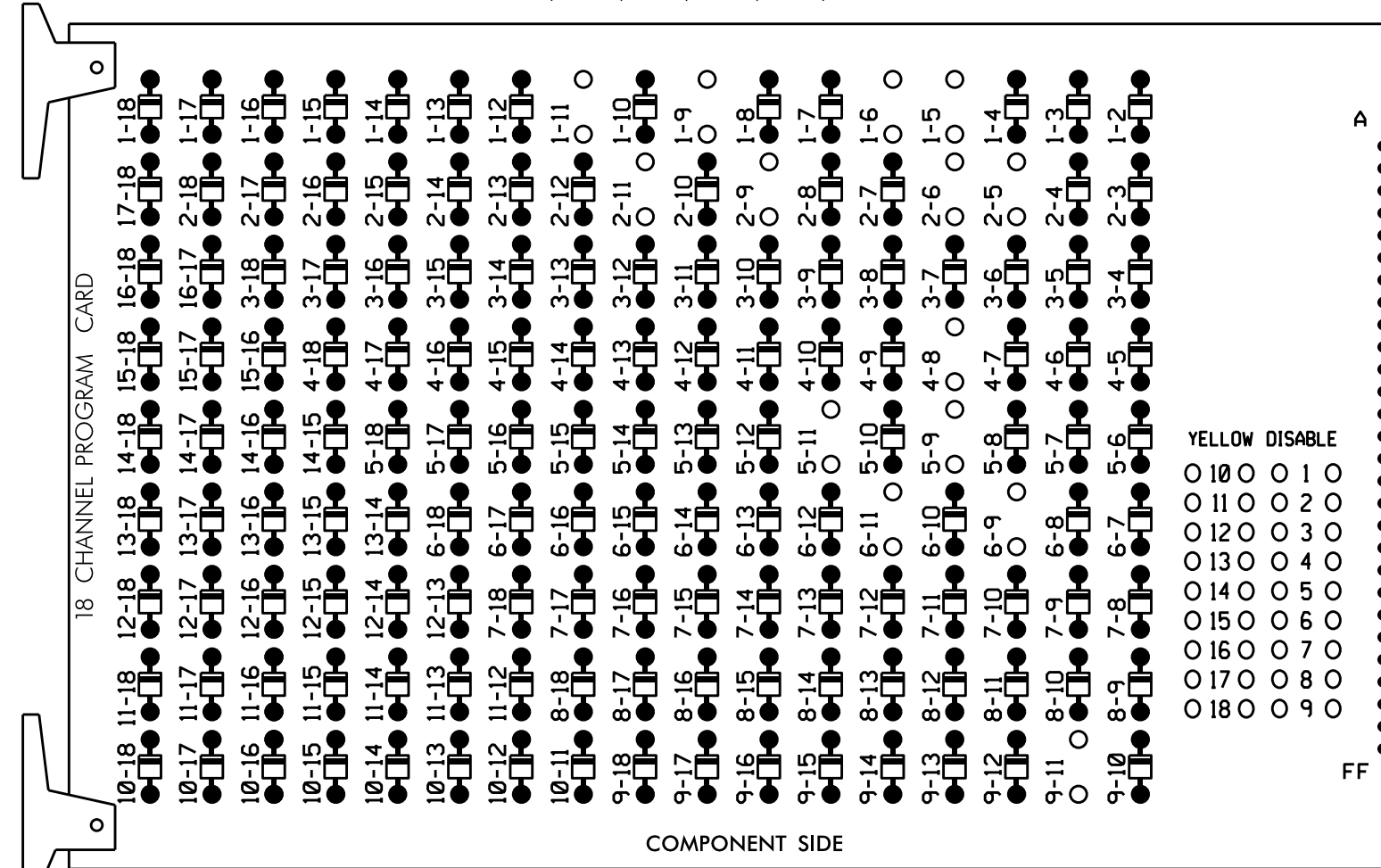
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 rz1:bergo



**EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11.

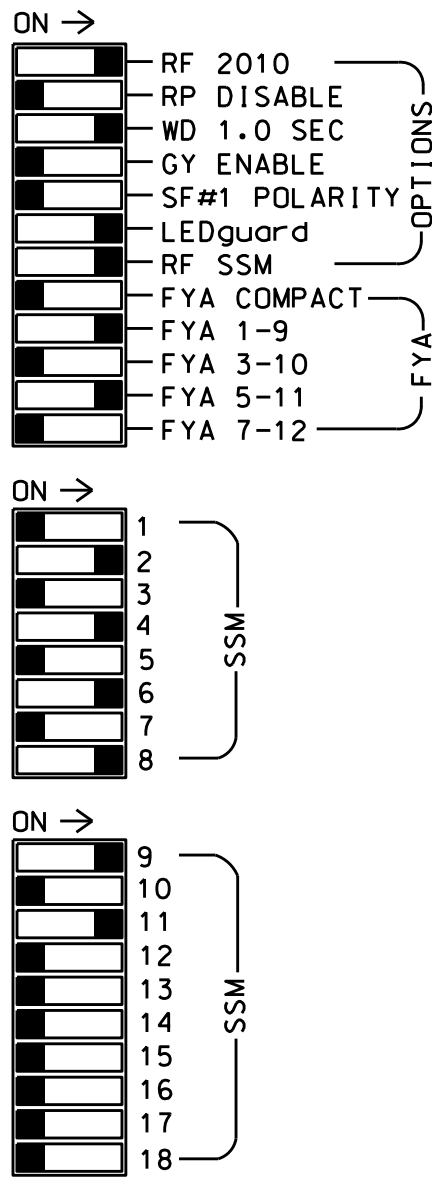
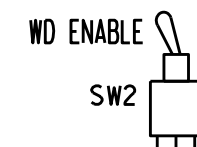


REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

ON OFF



■ = DENOTES POSITION OF SWITCH

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11,AUX S1,AUX S4  
 PHASES USED.....1,2,4,5,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....NOT USED

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	22,23	NU	NU	41,42,43	NU	51	62,63	NU	NU	81,82,83	NU	11	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127							133										

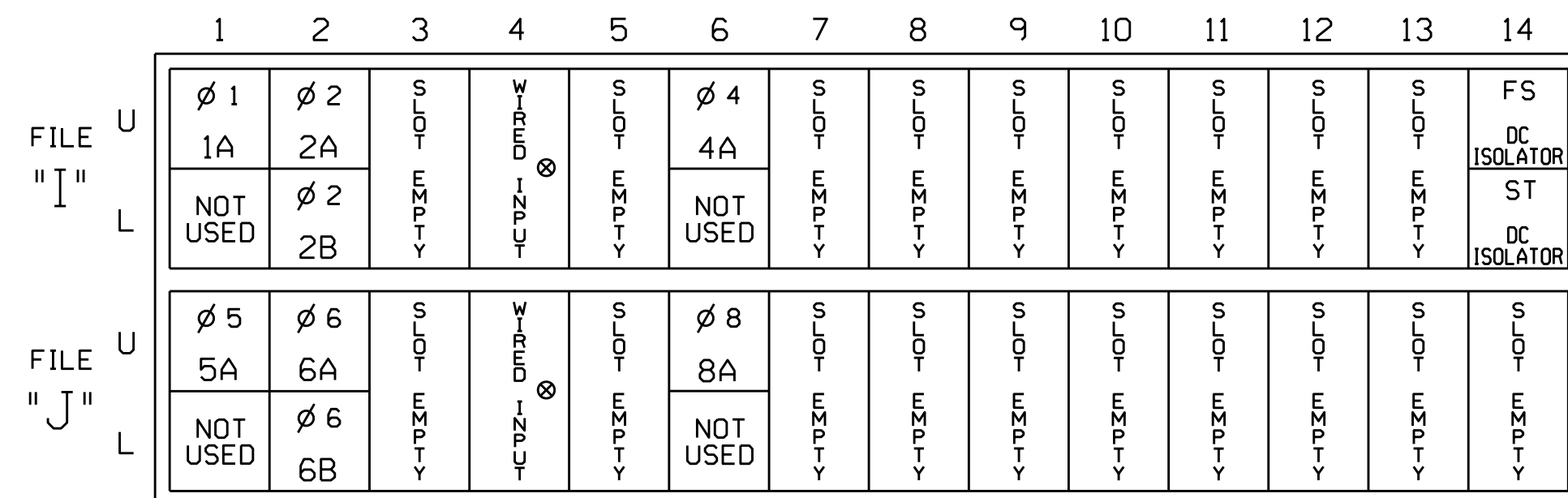
NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

**INPUT FILE CONNECTION & PROGRAMMING CHART**

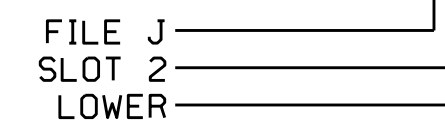
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			
2A	TB2-5,6	I2U	39	1	2	Y	Y				
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y	Y		15
	-	I4U	47	9★	22	2	Y	Y	Y		3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

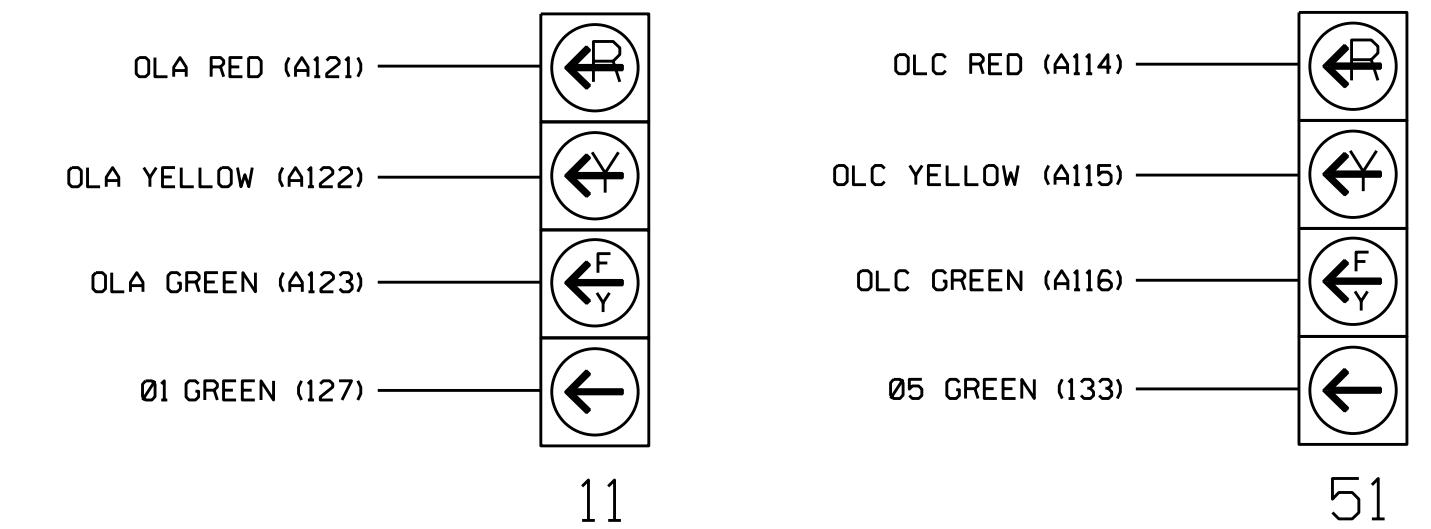
★ See Input Page Assignment programming details on sheets 3 and 4.

**INPUT FILE POSITION LEGEND: J2L**



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)



**NOTE**

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

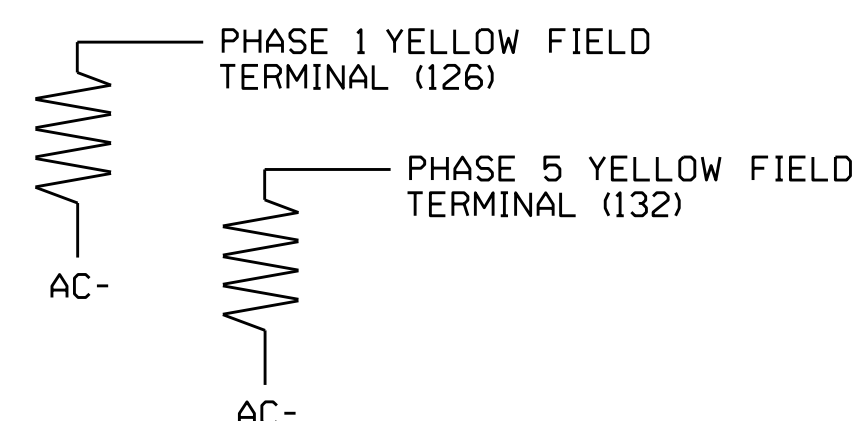
**THIS ELECTRICAL DETAIL SUPERSEDES THE ELECTRICAL DETAIL SEALED ON 6/6/17.**

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0208  
 DESIGNED: May 2017  
 SEALED: 2/20/2018  
 REVISED: N/A

**LOAD RESISTOR INSTALLATION DETAIL**

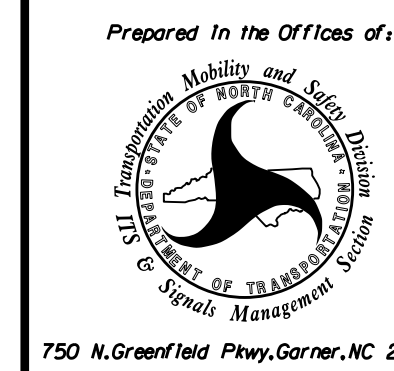
(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 5

ELECTRICAL AND PROGRAMMING DETAILS FOR:



US 1  
 at  
 SR 1127 (Pocomoke Road)

Division 5 Franklin County Franklinton

PLAN DATE: February 2018 REVIEWED BY:

PREPARED BY: S. Armstrong REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: Keith M. Mims 3/5/2018

SIG. INVENTORY NO. 05-0208

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL  
 NORTH CAROLINA PROFESSIONAL ENGINEER  
 KEITH M. MIMS  
 036880  
 DATE 3/5/2018

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL  
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

LOGICAL I/O COMMAND #1 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON  
AND RED CLEAR ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #50 ON  
SET OUTPUT ASSIGNMENT #51 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

LOGICAL I/O COMMAND #2 (+/-COMMAND#)  
IF ACTIVE PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #52 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #3 (+/-COMMAND#)  
IF YELLOW ON PHASE #1 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #51 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGICAL I/O COMMAND #4 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON  
AND RED CLEAR ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #42 ON  
SET OUTPUT ASSIGNMENT #43 OFF

PRESS '+'

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

LOGICAL I/O COMMAND #5 (+/-COMMAND#)  
IF ACTIVE PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #44 OFF

PRESS '+'

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

LOGICAL I/O COMMAND #6 (+/-COMMAND#)  
IF YELLOW ON PHASE #5 IS ON

↓  
SCROLL DOWN

THEN:  
SET OUTPUT ASSIGNMENT #43 ON

PRESS '+'

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL  
FOR DEFAULT PHASING**

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: :12345678910111213141516  
VEH OVL PARENTS: :XX  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: :12345678910111213141516  
VEH OVL PARENTS: : XX  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

**OVERLAP PROGRAMMING DETAIL  
FOR ALTERNATE PHASING**

(program controller as shown below)

- FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE PAGE 2

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS  
PHASE: :12345678910111213141516  
VEH OVL PARENTS: :X  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

PRESS '+' TWICE

NOTICE PAGE 2

PAGE 2: VEHICLE OVERLAP 'C' SETTINGS  
PHASE: :12345678910111213141516  
VEH OVL PARENTS: : X  
VEH OVL NOT VEH: :  
VEH OVL NOT PED: :  
VEH OVL GRN EXT: :  
STARTUP COLOR: - RED - YELLOW - GREEN  
FLASH COLORS: - RED - YELLOW X GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)  
FLASH YELLOW IN CONTROLLER FLASH?...Y  
GREEN EXTENSION (0-255 SEC)...0  
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0  
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0  
OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL SUPERSEDES THE ELECTRICAL DETAIL SEALED ON 6/6/17.

**OUTPUT REFERENCE SCHEDULE**

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0208  
DESIGNED: May 2017  
SEALED: 2/20/2018  
REVISED: N/A

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Electrical Detail - Sheet 2 of 5

	DETAILS FOR: <b>US 1 at SR 1127 (Pocomoke Road)</b>		
	Division 5 Franklin County Franklinton	PLAN DATE: February 2018 REVIEWED BY:	
REVISIONS		INIT. DATE	DocuSigned by: Keith M. Miras 3/5/2018 2F80786E8C03445 DATE
750 N. Greenfield Pkwy, Garner, NC 27529		SIG. INVENTORY NO. 05-0208	SEAL

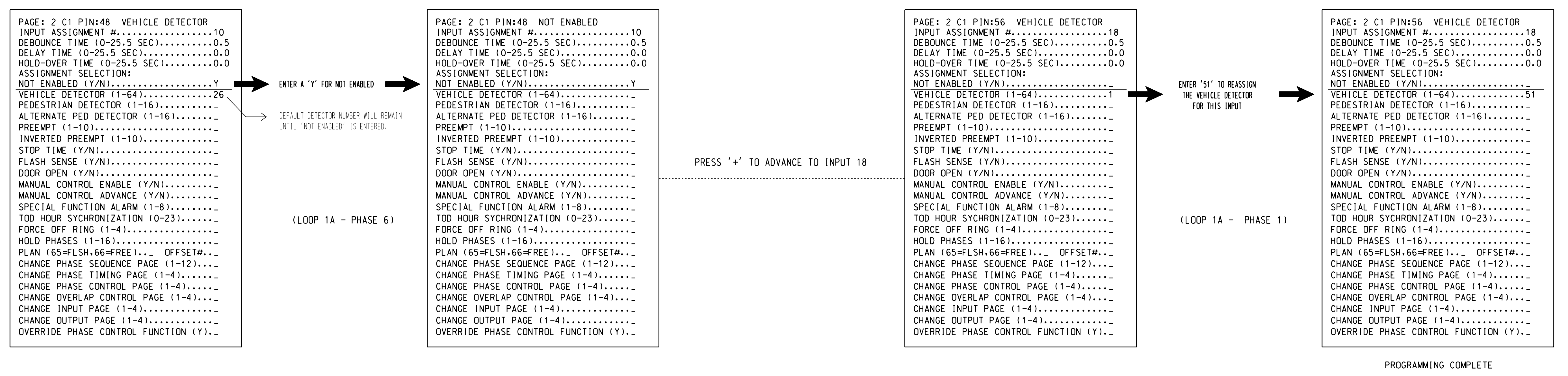


**INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A**

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

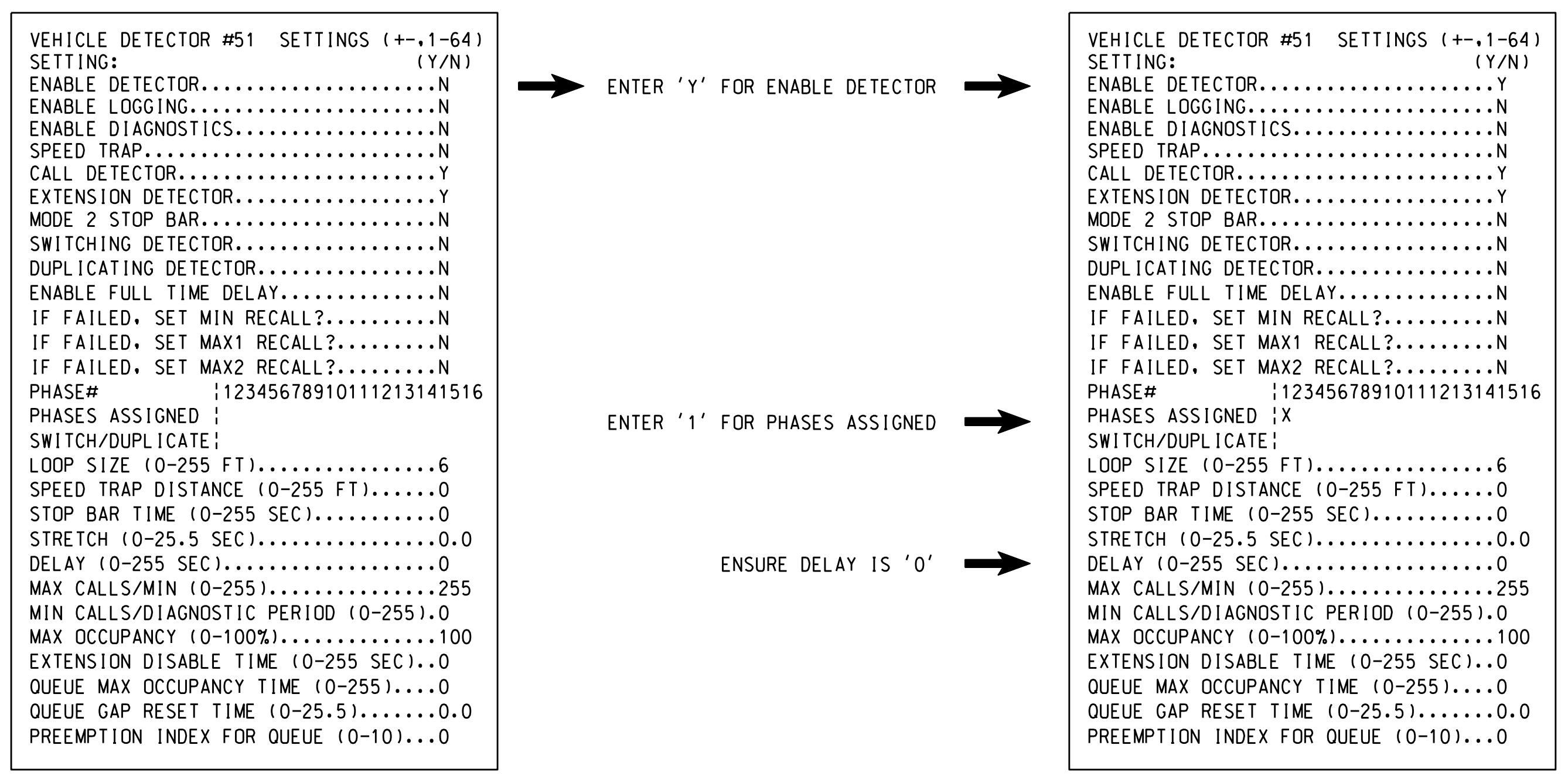
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.



**SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)**

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

**THIS ELECTRICAL DETAIL SUPERSEDES THE ELECTRICAL DETAIL SEALED ON 6/6/17.**

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0208  
DESIGNED: May 2017  
SEALED: 2/20/2018  
REVISED: N/A

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Electrical Detail - Sheet 3 of 5

Electrical and Programming Details for: **US 1 at SR 1127 (Pocomoke Road)**

Division 5 Franklin County Franklinton

PLAN DATE: February 2018 REVIEWED BY: [Signature]

PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

750 N. Greenfield Pkwy, Garner, NC 27529

Division 5 Professional Engineer Seal: Keith M. Mims, No. 036880

DocuSigned by: Keith M. Mims 3/5/2018

SIG. INVENTORY NO. 05-0208

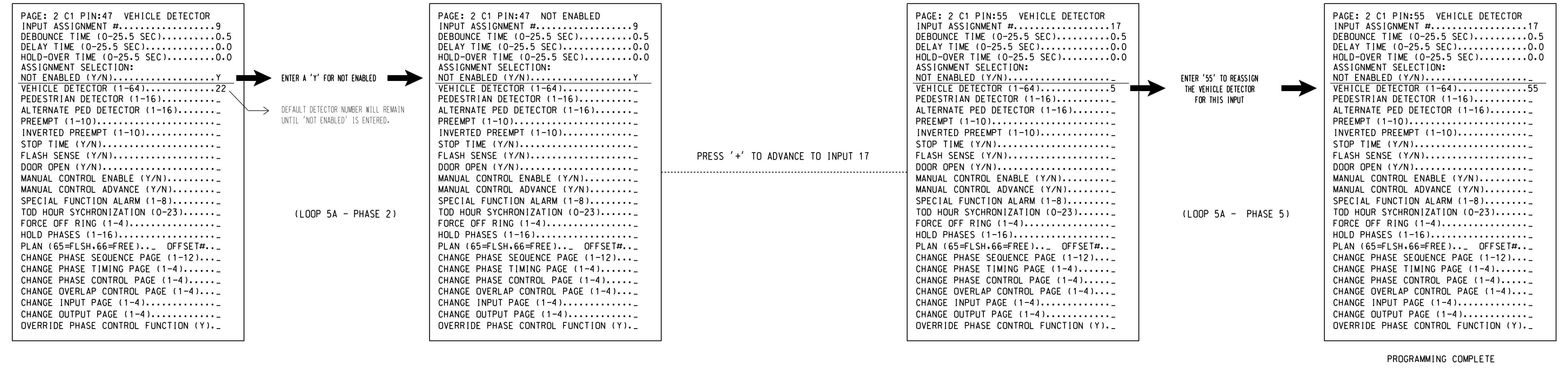


INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 5A

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #9 (DETECTOR 22) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 2 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 55 TO INPUT #17 SO THAT THE DELAY ON LOOP 5A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

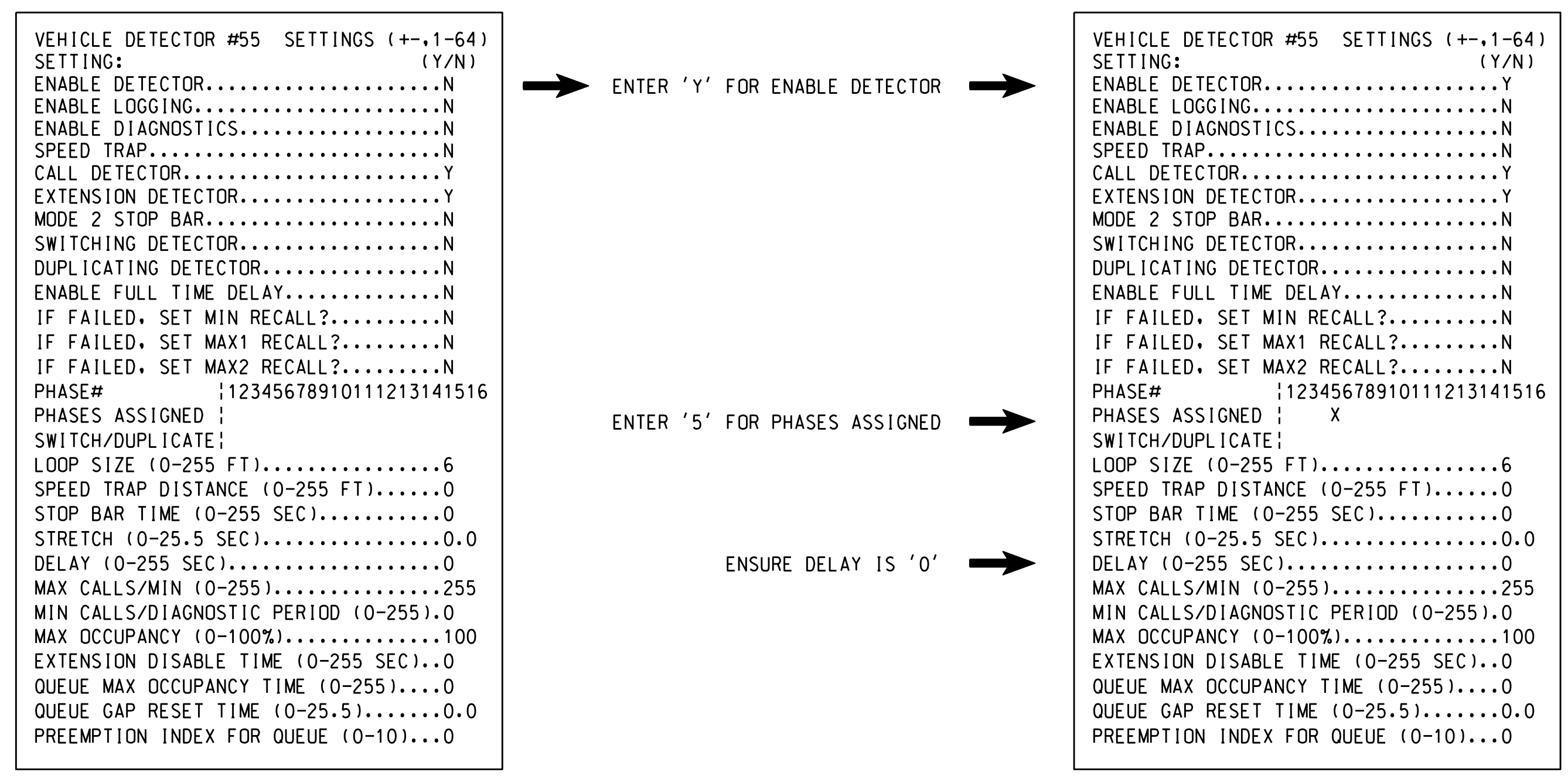
FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 9 IS REACHED.



SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 5A (ALT.)

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #55.



NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

THIS ELECTRICAL DETAIL SUPERSEDES THE ELECTRICAL DETAIL SEALED ON 6/6/17.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0208  
DESIGNED: May 2017  
SEALED: 2/20/2018  
REVISED: N/A

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Electrical Detail - Sheet 4 of 5

Prepared In the Offices of:  
  
 750 N. Greenfield Pkwy, Garner, NC 27529

US 1 at SR 1127 (Pocomoke Road)

Division 5 Franklin County Franklinton

PLAN DATE: February 2018 REVIEWED BY: [Signature]  
 PREPARED BY: S. Armstrong REVIEWED BY: [Signature]

REVISIONS	INIT.	DATE

DocuSigned by:  
  
 Keith M. Mims 3/5/2018

SEAL  
 NORTH CAROLINA PROFESSIONAL ENGINEER  
 KEITH M. MIMS  
 036880

SIG. INVENTORY NO. 05-0208

## ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

<u>PHASING</u>	<u>INPUTS PAGE</u>	<u>OVERLAPS PAGE</u>
ACTIVE PAGES REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	1
ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	2

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

### ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAPS PAGE 2: Modifies overlap parent phases for heads 11 and 51 to run protected turns only.


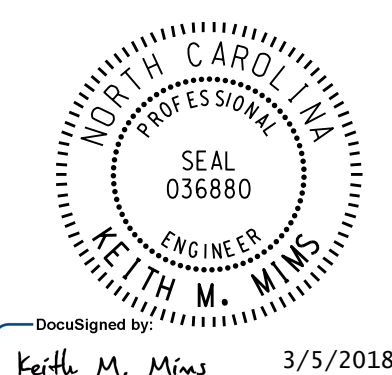
INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

THIS ELECTRICAL DETAIL SUPERSEDES THE ELECTRICAL DETAIL SEALED ON 6/6/17.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-0208  
DESIGNED: May 2017  
SEALED: 2/20/2018  
REVISED: N/A

Electrical Detail - Sheet 5 of 5

<p>ELECTRICAL AND PROGRAMMING DETAILS FOR:</p> <p style="text-align: center; font-size: small;">Prepared In the Offices of:</p>  <p style="font-size: x-small;">750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p><b>US 1</b> at <b>SR 1127 (Pocomoke Road)</b></p> <p>Division 5 Franklin County Franklinton</p> <p>PLAN DATE: February 2018    REVIEWED BY:</p> <p>PREPARED BY: S. Armstrong    REVIEWED BY:</p> <table border="1" style="width: 100%; border-collapse: collapse; font-size: x-small;"> <thead> <tr> <th>REVISIONS</th> <th>INIT.</th> <th>DATE</th> </tr> </thead> <tbody> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> </tbody> </table>	REVISIONS	INIT.	DATE										<p style="text-align: center; font-weight: bold;">DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p> <p style="text-align: center;">SEAL</p>  <p style="font-size: x-small;">DocuSigned by: <b>Keith M. Miras</b>    3/5/2018</p> <p style="font-size: x-small;">2F8079E8E6CD3445    DATE</p> <p style="font-size: x-small;">SIG. INVENTORY NO. 05-0208</p>
REVISIONS	INIT.	DATE												